FIG.1

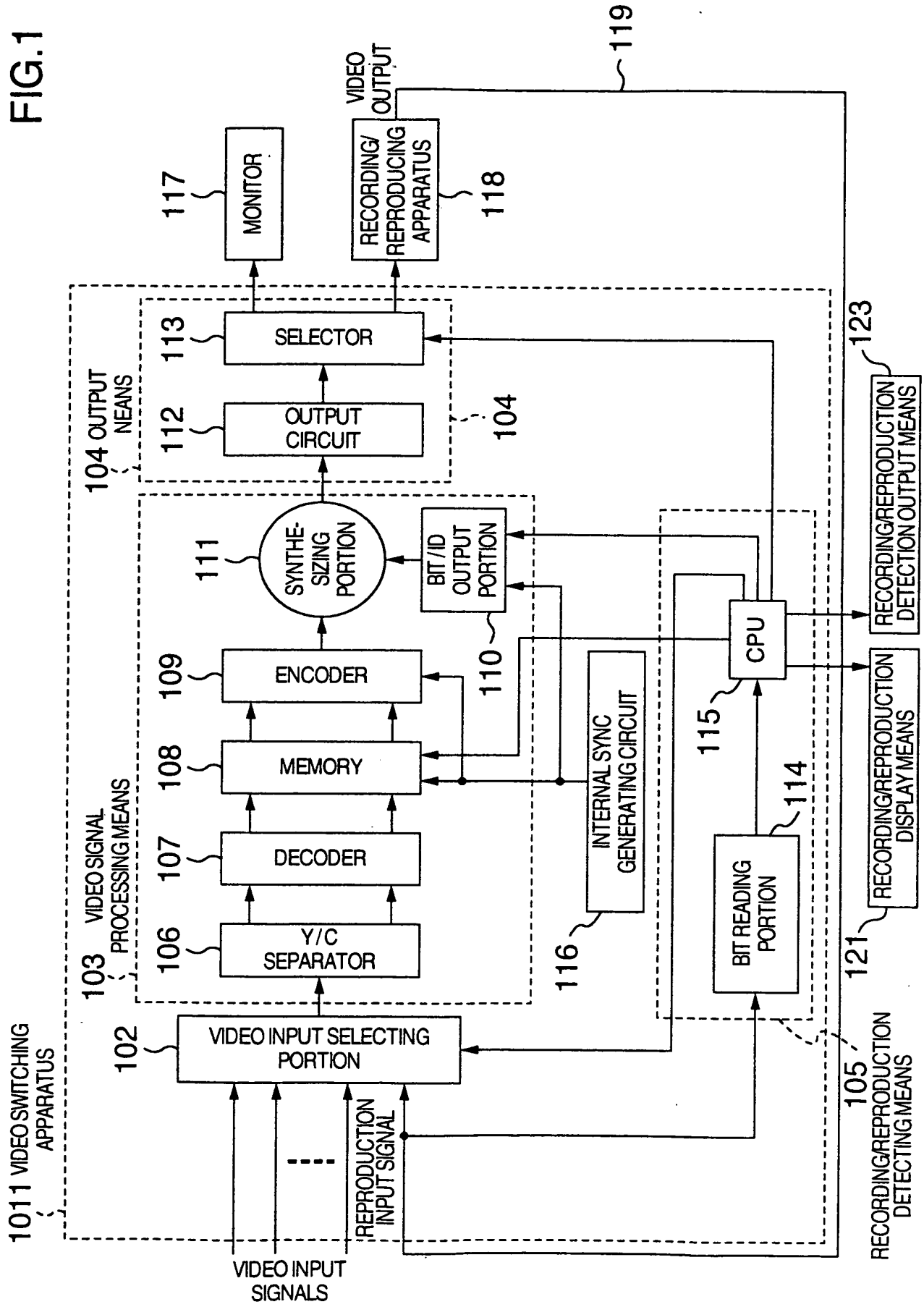

1011 VIDEO SWITCHING APPARATUS
VIDEO INPUT SIGNALS
REPRODUCTION INPUT SIGNAL
102 VIDEO INPUT SELECTING PORTION
103 VIDEO SIGNAL PROCESSING MEANS
106 Y/C SEPARATOR
107 DECODER
108 MEMORY
109 ENCODER
111 SYNTHE-SIZING PORTION
110 BIT/ID OUTPUT PORTION
104 OUTPUT MEANS
112 OUTPUT CIRCUIT
113 SELECTOR
117 MONITOR
118 RECORDING/ REPRODUCING APPARATUS
VIDEO OUTPUT
119
116 INTERNAL SYNC GENERATING CIRCUIT
115 CPU
114 BIT READING PORTION
121 RECORDING/REPRODUCTION DISPLAY MEANS
123 RECORDING/REPRODUCTION DETECTION OUTPUT MEANS
105 RECORDING/REPRODUCTION DETECTING MEANS

FIG.2

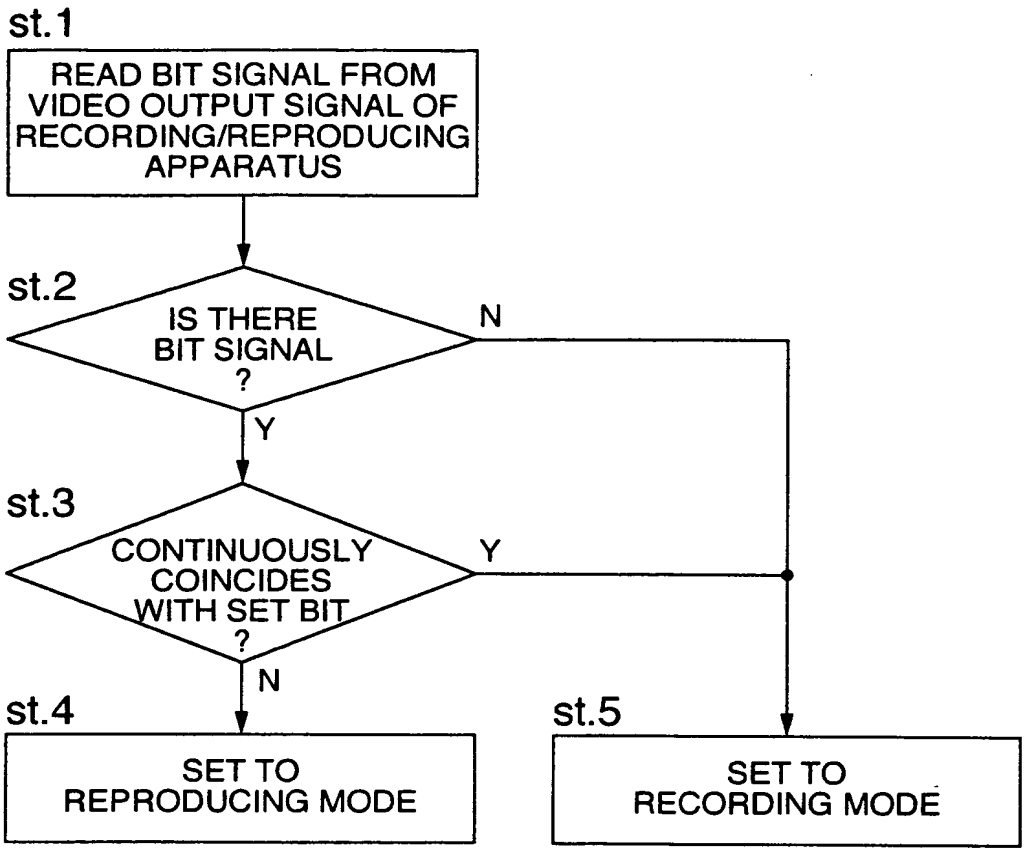

st.1
READ BIT SIGNAL FROM VIDEO OUTPUT SIGNAL OF RECORDING/REPRODUCING APPARATUS
st.2
IS THERE BIT SIGNAL ?
N
Y
st.3
CONTINUOUSLY COINCIDES WITH SET BIT ?
Y
N
st.4
SET TO REPRODUCING MODE
st.5
SET TO RECORDING MODE

FIG.3

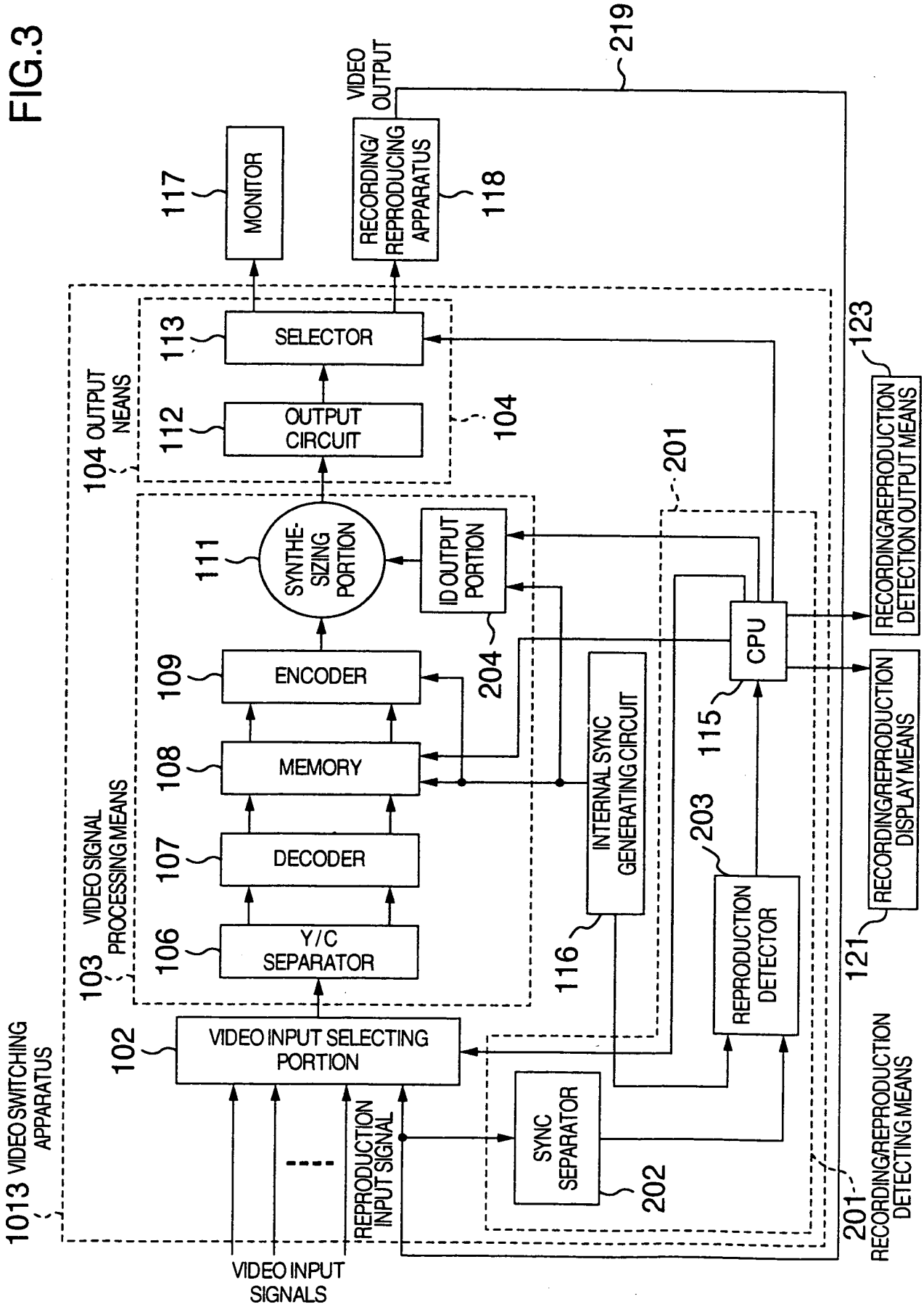

1013 VIDEO SWITCHING APPARATUS
VIDEO INPUT SIGNALS
REPRODUCTION INPUT SIGNAL
102 VIDEO INPUT SELECTING PORTION
103 VIDEO SIGNAL PROCESSING MEANS
106 Y/C SEPARATOR
107 DECODER
108 MEMORY
109 ENCODER
111 SYNTHE-SIZING PORTION
104 OUTPUT MEANS
112 OUTPUT CIRCUIT
113 SELECTOR
117 MONITOR
118 RECORDING/ REPRODUCING APPARATUS
VIDEO OUTPUT
219
ID OUTPUT PORTION
204
INTERNAL SYNC GENERATING CIRCUIT
116
CPU
115
201
RECORDING/REPRODUCTION DETECTION OUTPUT MEANS
123
RECORDING/REPRODUCTION DISPLAY MEANS
121
203
REPRODUCTION DETECTOR
SYNC SEPARATOR
202
201 RECORDING/REPRODUCTION DETECTING MEANS

FIG.4

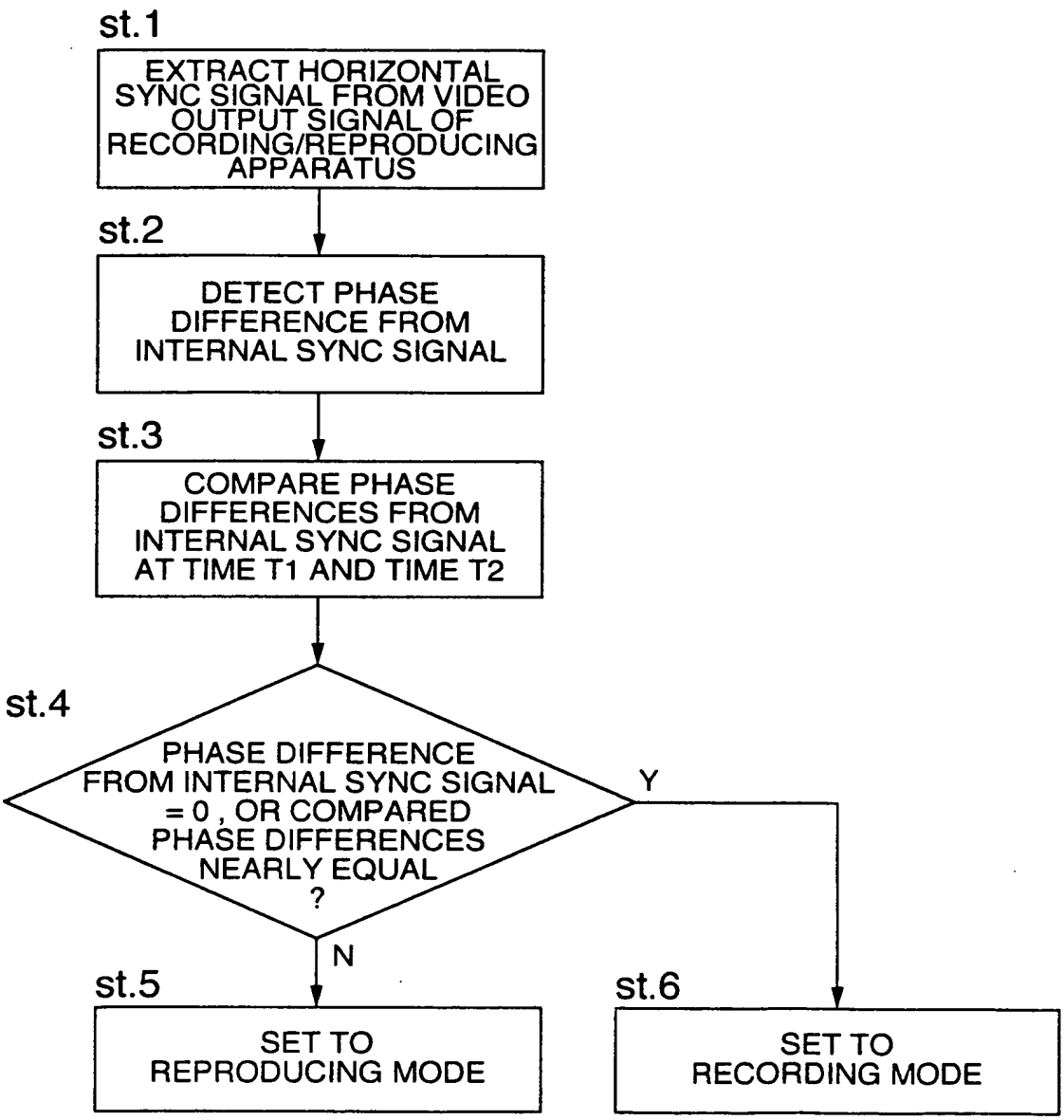
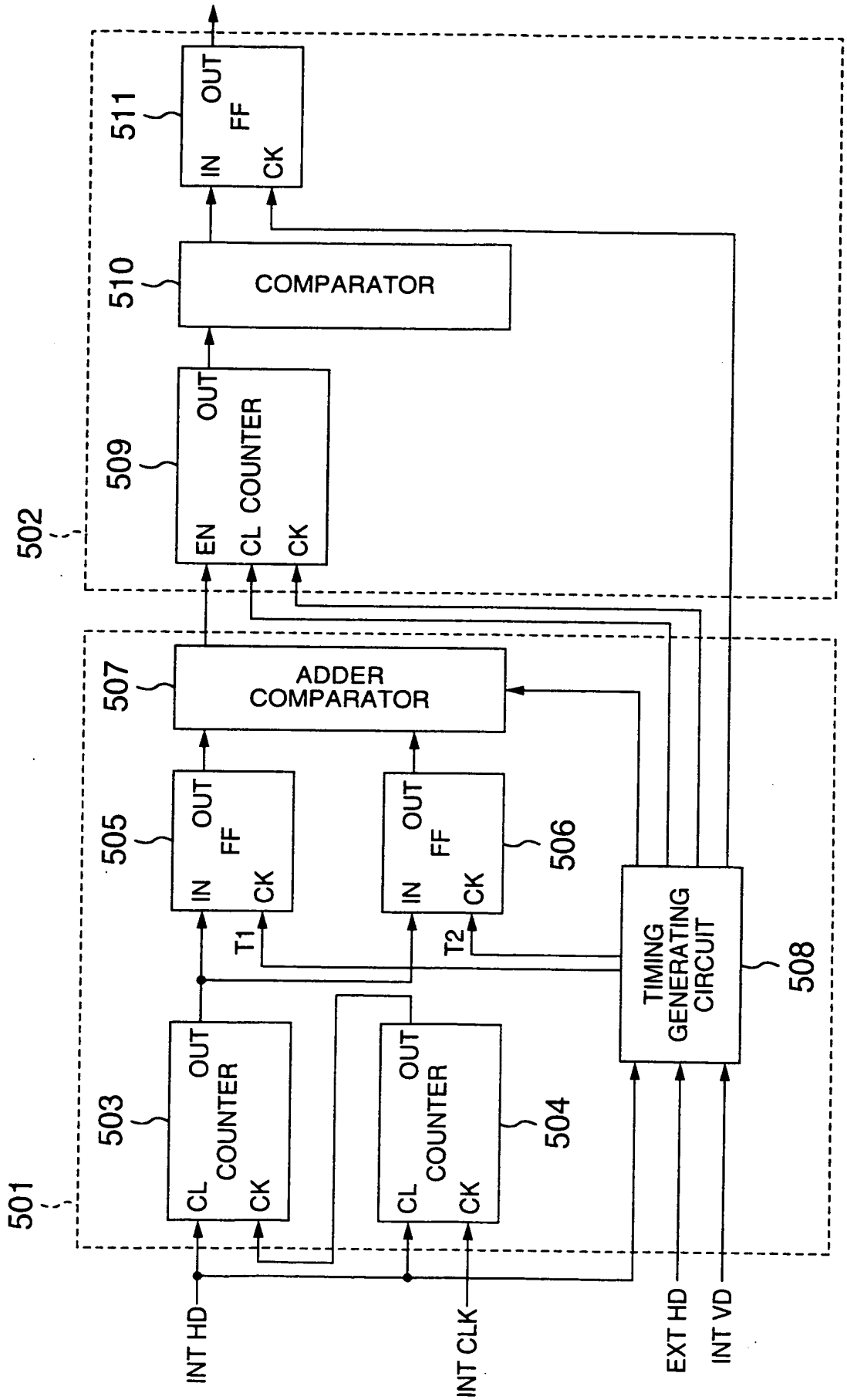

st.1
EXTRACT HORIZONTAL SYNC SIGNAL FROM VIDEO OUTPUT SIGNAL OF RECORDING/REPRODUCING APPARATUS
st.2
DETECT PHASE DIFFERENCE FROM INTERNAL SYNC SIGNAL
st.3
COMPARE PHASE DIFFERENCES FROM INTERNAL SYNC SIGNAL AT TIME T1 AND TIME T2
st.4
PHASE DIFFERENCE FROM INTERNAL SYNC SIGNAL = 0 , OR COMPARED PHASE DIFFERENCES NEARLY EQUAL ?
Y
N
st.5
SET TO REPRODUCING MODE
st.6
SET TO RECORDING MODE

FIG.5


501
502
503
504
505
506
507
508
509
510
511
CL COUNTER CK
OUT
IN FF CK
ADDER COMPARATOR
TIMING GENERATING CIRCUIT
T1
T2
EN CL COUNTER CK
COMPARATOR
INT HD
INT CLK
EXT HD
INT VD